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APPLICANTS

Hideo Akiyoshi, Kawasaki, JAPAN;

** CONTINUING DATA ***** *YES* *****
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** FOREIGN APPLICATIONS ***** *NO* *****
JAPAN 11-192375 07/06/1999

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 8	TOTAL CLAIMS 10	INDEPENDENT CLAIMS 6
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Verified and Acknowledged *Phan* Examiner's Signature *TP* Initials

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TITLE
Latch circuit having reduced input/output load memory and semiconductor chip

FILING FEE RECEIVED 992	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input checked="" type="checkbox"/> All Fees <input checked="" type="checkbox"/> 1.16 Fees (Filing) <input checked="" type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input checked="" type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____
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